

ABSTRACT OF THE DISCLOSURE

In a buffer that outputs an analog voltage V_{out} to be applied as a driving voltage to a pixel capacitance in a display region of an active-matrix liquid crystal display device, a CMOS circuit for generating this analog voltage includes four Pch transistors (QP0 to QP3) connected in parallel and four Nch transistors (QN0 to QN3) connected in parallel. When charging the pixel capacitance, a bias current is reduced and the driving capability is lowered by control with selector switches (SP_1 to SP_3) at a time at which the 10 large driving capability at the beginning of the charging is not necessary anymore. And when discharging the charge that has accumulated at the pixel capacitance, the bias current is reduced and the driving capability is lowered by control with selector switches (SN_1 to SN_3) at a time at which the large driving capability at the beginning of the discharging is not necessary 15 anymore. With this configuration, it is possible to reduce the power consumption of the output buffer applying the analog voltage for image display to the capacitive load in the display panel.